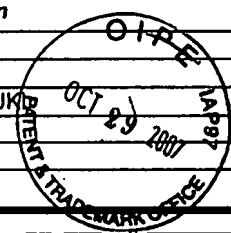


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Sheet	1	of	1		



U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
P20	A1	US-5,815,023	9-29-1998	WEBBER et al.	Corresponds to Ref. A2

UNPUBLISHED U.S. PATENT APPLICATION DOCUMENTS					
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P20	A2	JP-10-275075	10-13-1998	-SUN MICROSYSTEMS, INC.	Corresponds to Ref. A1	
P20	A3	JP-2-277125	11-13-1990	-TOSHIBA CORP -TOSHIBA MICRO ELECTRON KK		✓
P20	A4	JP-7-38398	2-7-1995	-NEC CORP		✓
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P20	A6	JP-5-206791	8-13-1993	-NIPPON DENKI IC MICON SYSTEM		✓
P20	A7	JP-7-262003	10-13-1995	-HITACHI GAZO JOHO SYSTEM KK -HITACHI LTD		✓

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
P20	A8	John L. Hennessy, B.5 Memory Elements, Computer Organization and Design, 2 nd Edition (Volume 2), Japan, Nikkei Business Publications, Inc., Hisashi Okamura, May 17, 1999, 2 nd Edition, pp. 799-804	
P20	A9	Japanese Office Action dated October 1, 2007, Japanese Patent Office, Application No. 2002-265521	

Examiner Signature		Date Considered	01/09/07
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